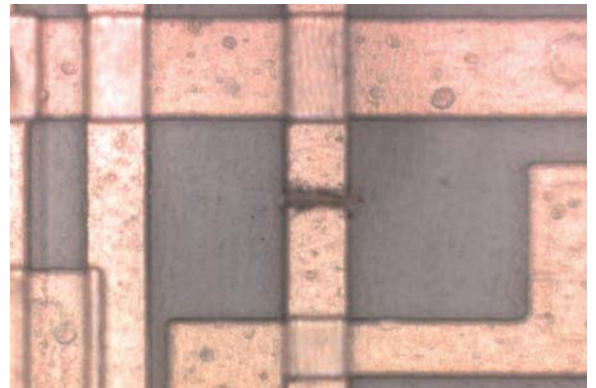
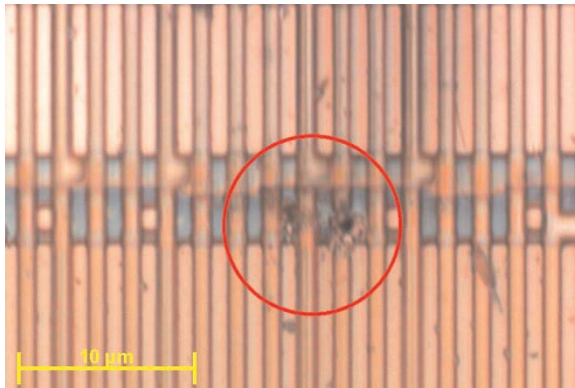




Circuit Isolation



Circuit isolation can be achieved on diffraction limited structures and at specific levels of integration without any collateral damage